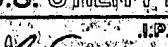
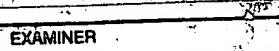


Subclass	ISSUE CLASSIFICATION	P		PATENT NUMBER
<b>U.S. UTILITY Patent Application</b>				
 <b>I.P.E.</b> <small>SCANNED</small>		<b>PATENT DATE</b> 		
APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT
09/902673	7 D F	438	745	12825
EXAMINER				
APPLICANTS	Michimasa Funabashi			
<b>BEST AVAILABLE COPY</b>				
Method of manufacturing a semiconductor integrated circuit device				
TITLE				
PTO-2040 12/69				

ISSUING CLASSIFICATION			
ORIGINAL	CROSS REFERENCE(S)		
CLASS	CLASS	SUBCLASSES (ONE SUBCLASS PER LINE BLOCK)	
ADDITIONAL CLASSIFICATION			
CONTINUED			
EFFECTIVE DATE		DRAWINGS	
01/01/1988		Block Draw.	Fig. Draw.
(000)		(000)	(000)
MATERIALS		PRINTED ALLOWED	
(000)		Total Sheets	Print Order for O.C.
(000)		(000)	(000)
NOTES		REMARKS	
(000)		(000)	
(000)		(000)	

FILED WITH:  Clerk (S.A.)  Clerk U.S. Court of Appeals  
for the Federal Circuit

(FACE)